ECN#

408

HW or Project Engineer ___

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Juilly ENGINEERING CHANGE NOTE:



ECN#: **ASSEMBLY:** BMW4011E **DESCRIPTION:** MW4-PXY CPU CARD New Assembly: DATE: 13/02/01 **Current Assy Rev:** 5.12 **Current Schematic Rev:** New Assy Rev: **New Schematic Rev:** 5.13 THIS ECN IS ESSENTIAL FOR RELIABLE OPERATION OF THE SYSTEM. • Excel BOMs Updated O Subcontractor Records Updated O CMS BOMs Updated Compatibility Maintained: Yes IMPLEMENTATION INFORMATION: Safety Issue UL Compliance Issue O New Feature **Cost Reduction Bug Fix** Ouality Issue **EMC Compliance Issue Reliability Reasons Software Change** Cosmetic Change **REASON FOR CHANGE:** Two tracks are missing on Rev 4 & 5 PCB-s: to U22/B8 (XDIO_CLK) and U22/B13 (YDIO_CLK). This can cause intermittent faults and potentially affect all writes to the IO devices from PX and PY. Observed faults include (but not limited to) PY not starting correctly. **DETAILS OF CHANGE:** 1. Connect U57/14 to U57/12. 2. Connect U62/14 to U62/12. 3. Place revision resistors (all 10k - RMD8573, 0.125W, 1%, 0603) as follows: R127 - Omit R128 - 10k R133 - 10k R134 - Omit R135 - Omit R136 - Omit R137 - 10k R138 - 10k 4. Place a assembly revision label on the card, marked "T", after the blank Rev 5 PCB P/N so it reads as "BMW4011E-T" NOTE TO SUBCONTRACTORS: This ECN applies to all boards in WIP. NOTE TO PRODUCTION: This ECN applies to all boards in WIP. NOTE TO FIELD SERVICE: All Rev 5 (E) PXY-s in service should be modified at the first opportunity.

R&D Manager